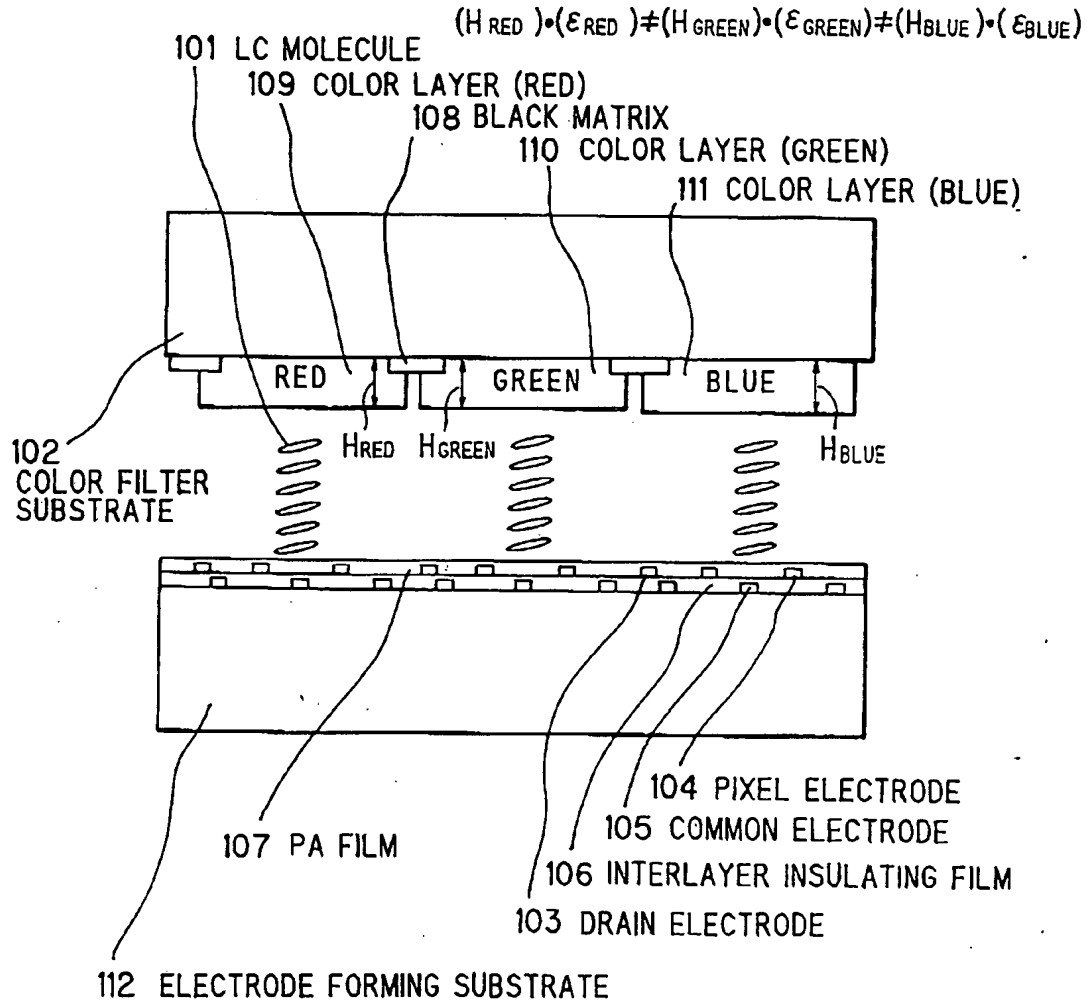
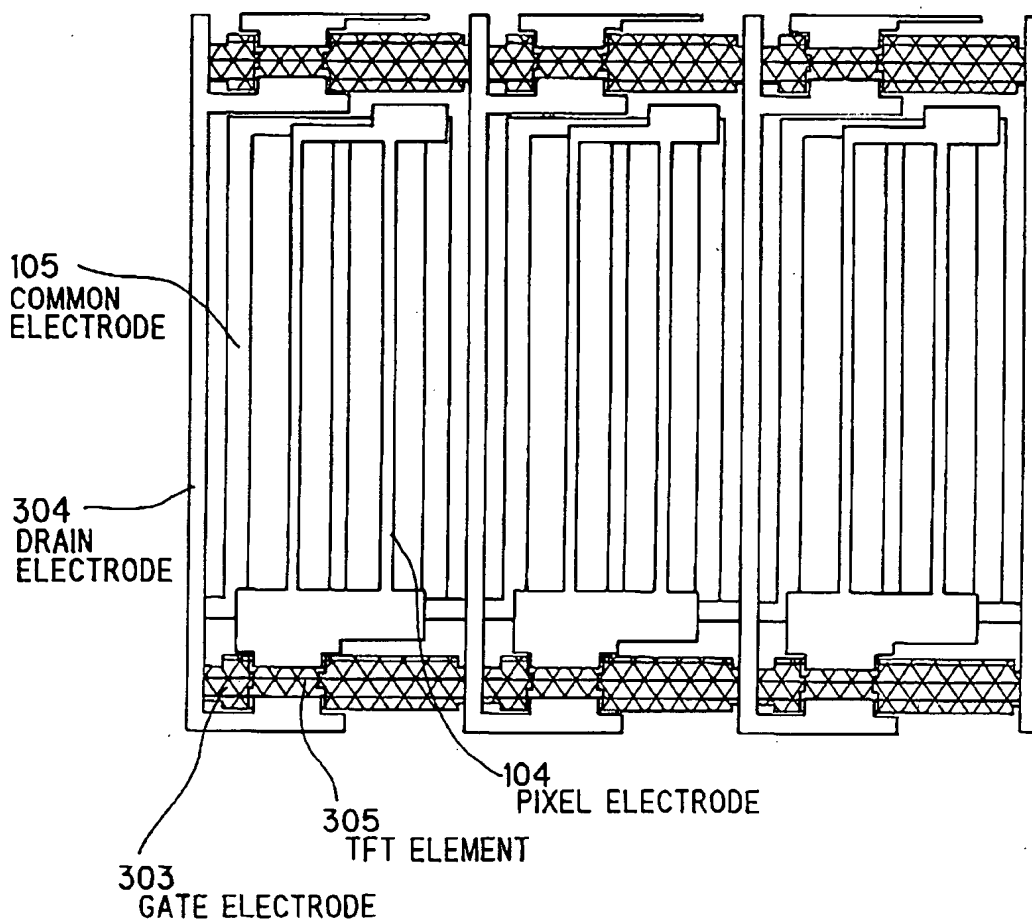


FIG. 1 PRIOR ART



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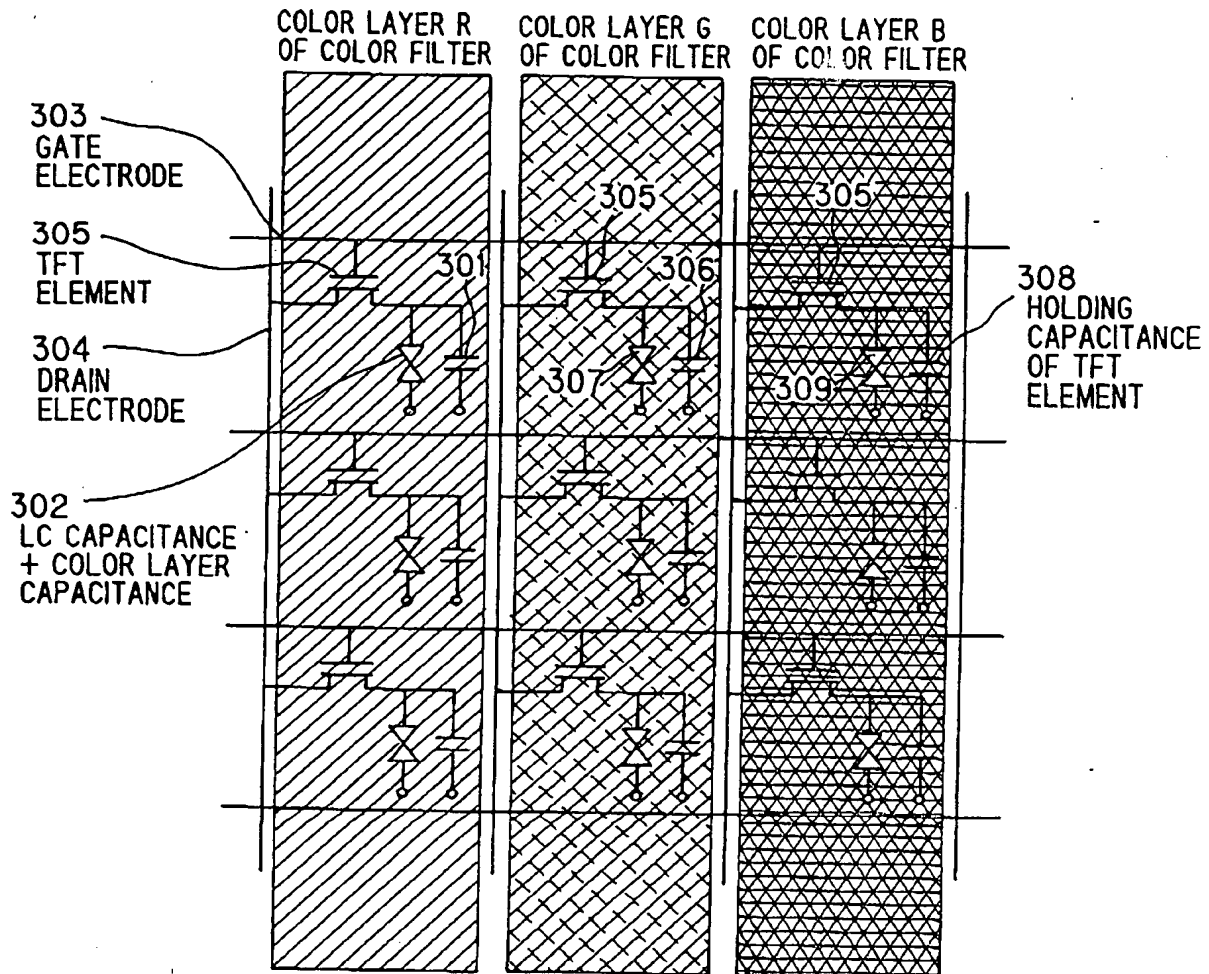
FIG.2 PRIOR ART



2000-08-01 00:00:00

[illegible]

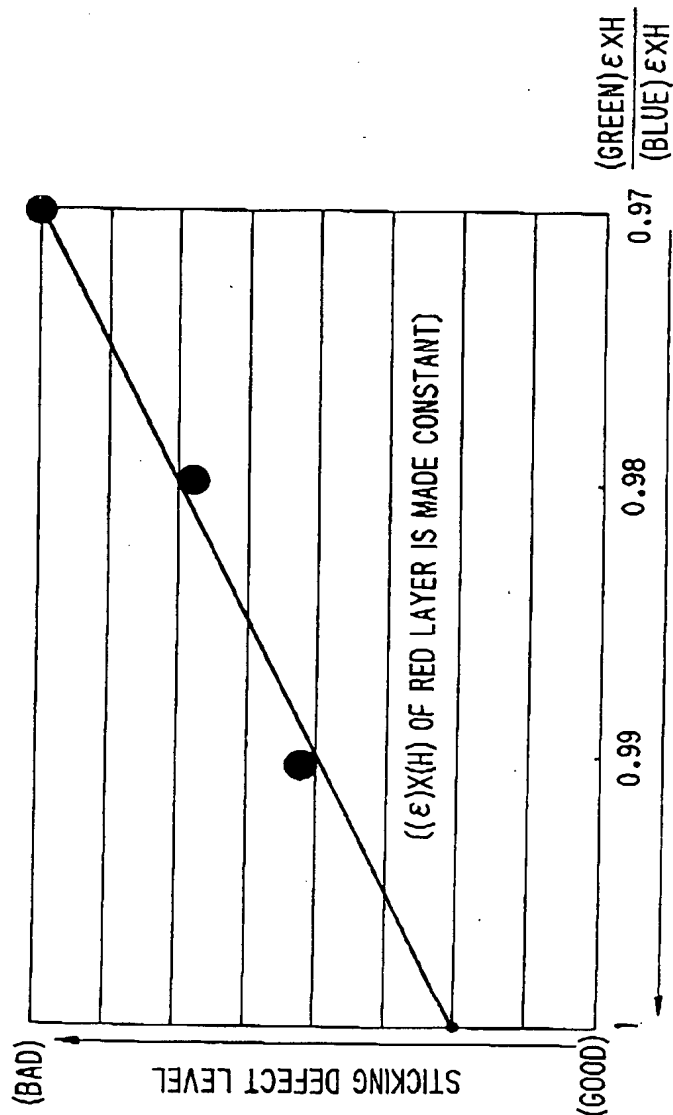
FIG. 4



Timing diagram for a three-color active matrix display. The diagram shows the drain voltage waveforms for three color layers: Color Layer R, Color Layer G, and Color Layer B. The horizontal axis represents time, and the vertical axis represents drain voltage V_D .

The waveforms are square waves centered at V_D^{center} . The peak voltages for each color layer are labeled V_1 , V_2 , and V_3 respectively. The condition $V_3 > V_2 > V_1$ is indicated in a box on the right.

FIG.6



DIRECTION WHERE "(COLOR-LAYER STATIC CAPACITANCE) + (LC STATIC CAPACITANCE)" OF RESPECTIVE COLOR LAYERS OF COLOR FILTER BECOMES EQUAL

(AT ABSCISSA POINT = 1, "(COLOR-LAYER STATIC CAPACITANCE) + (LC STATIC CAPACITANCE)" OF RESPECTIVE COLOR LAYERS ARE EQUAL)

FIG. 7

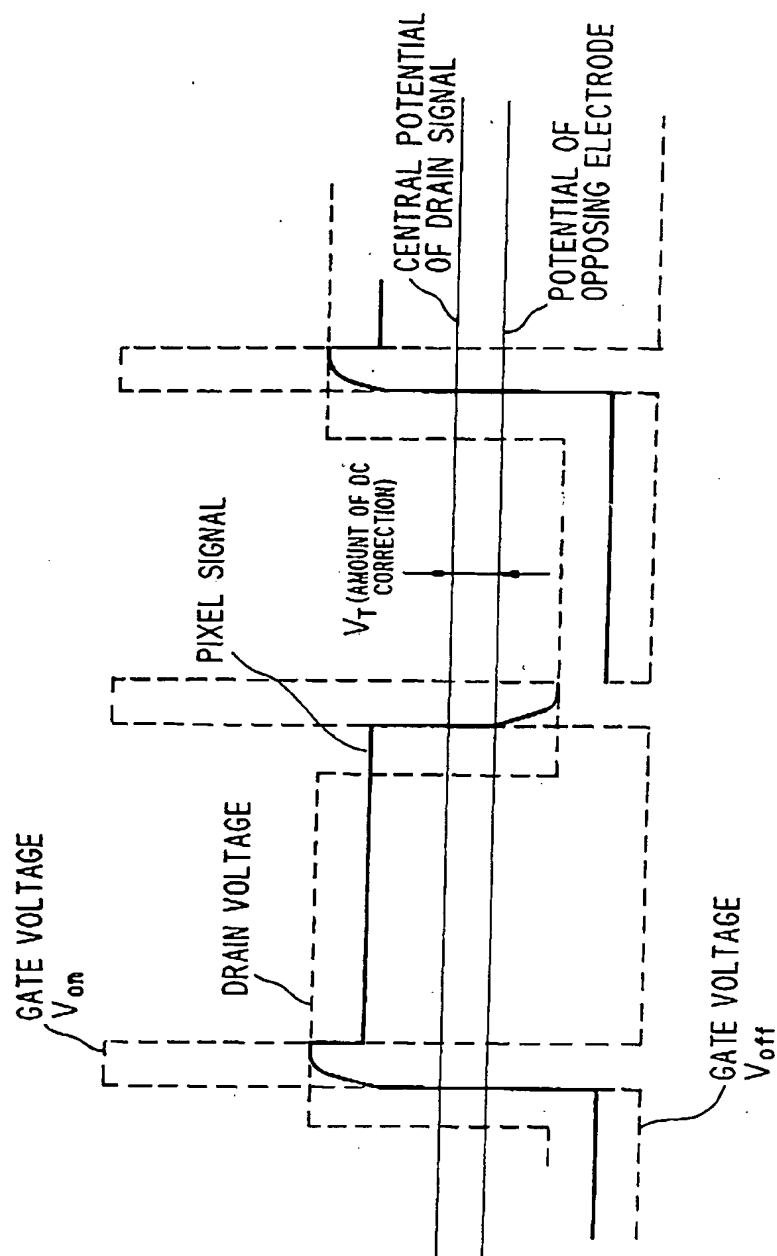


FIG. 8

